

Fig. 1

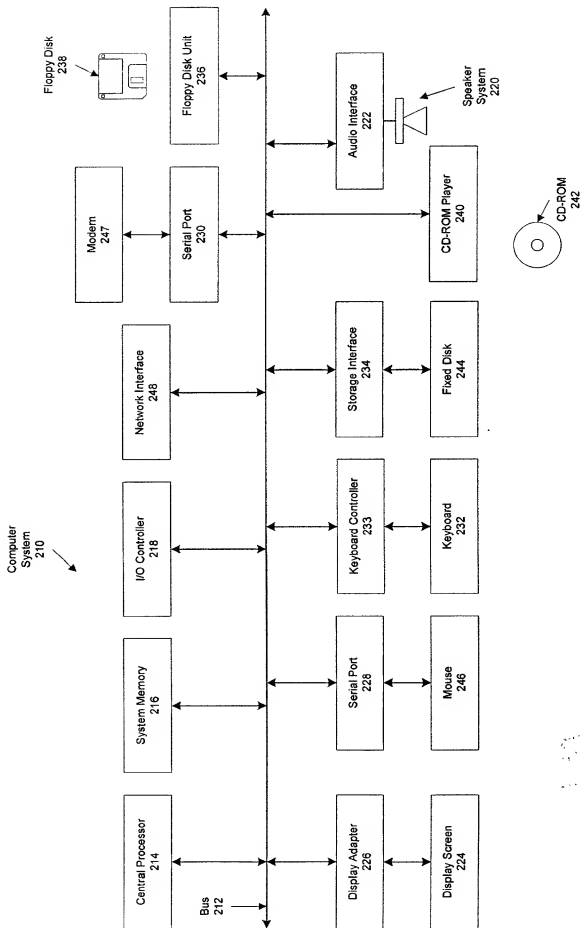


Fig. 2

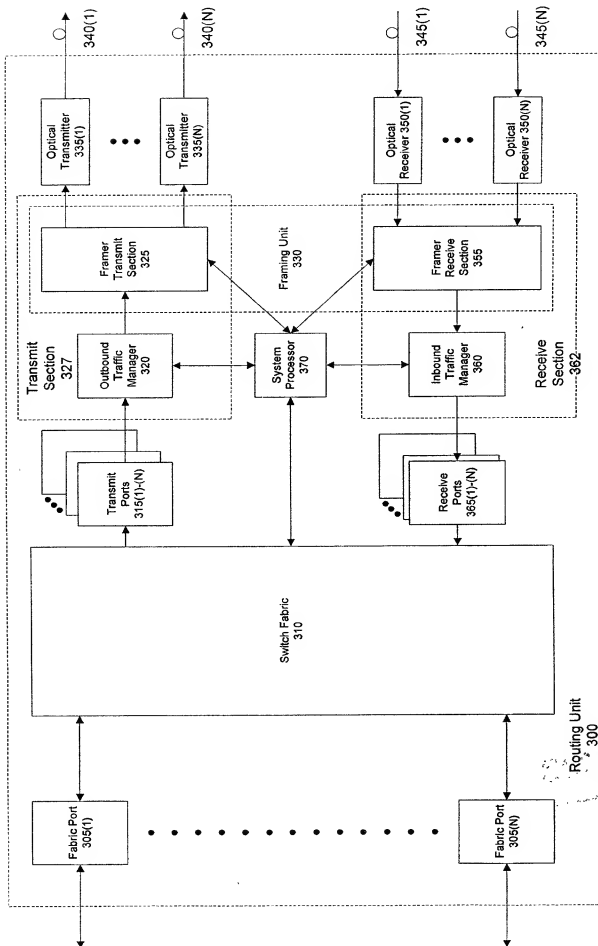


Fig. 3

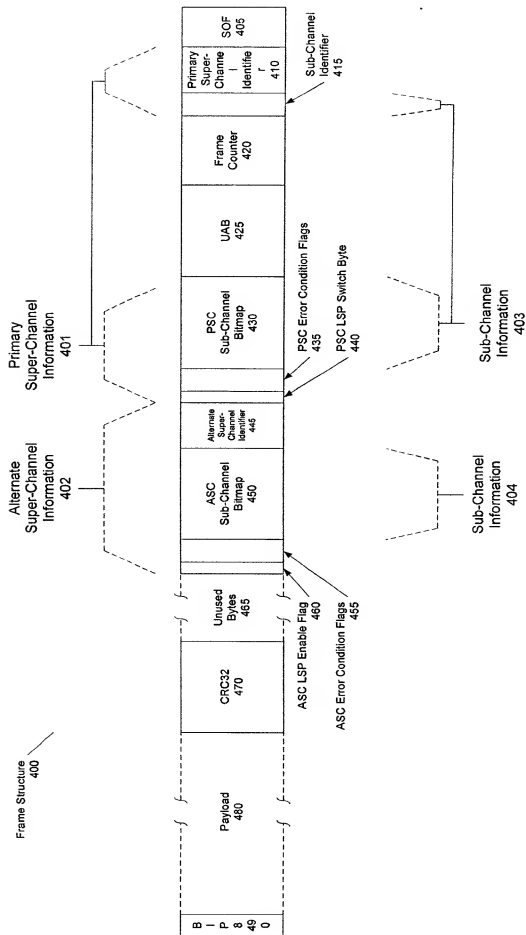


Fig. 4

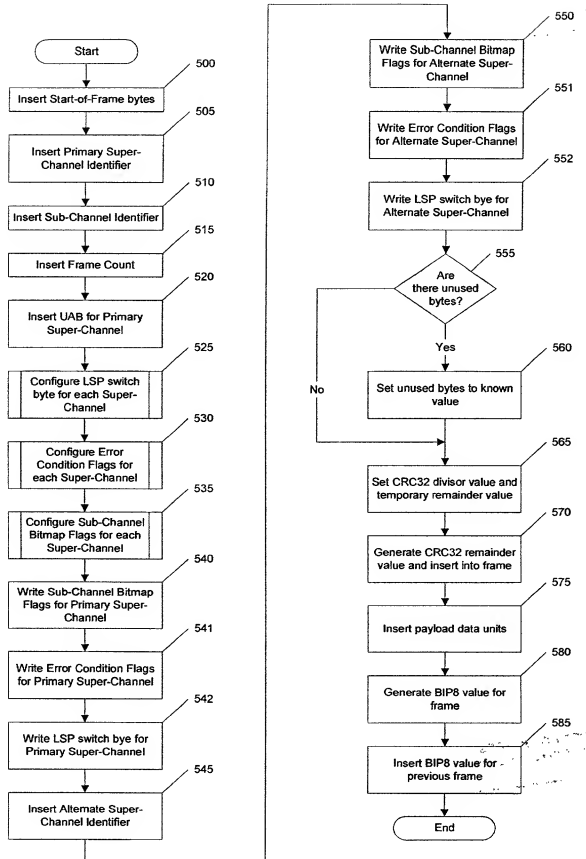


Fig. 5

10086273.07.1002

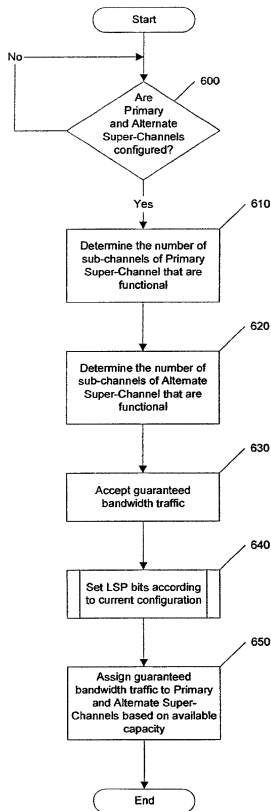


Fig. 6

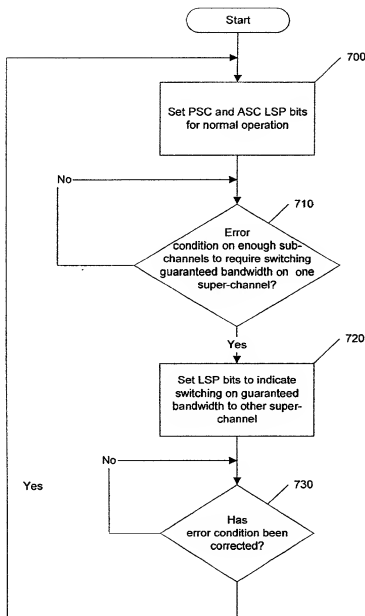


Fig. 7

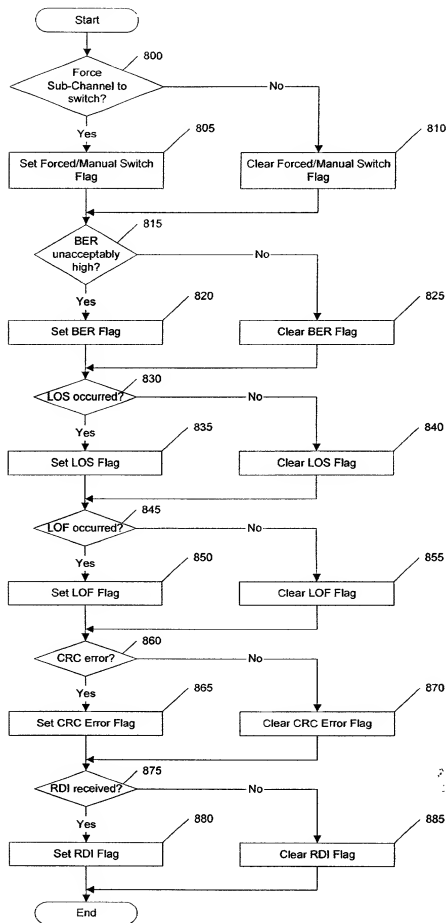


Fig. 8

10086273.07.1002

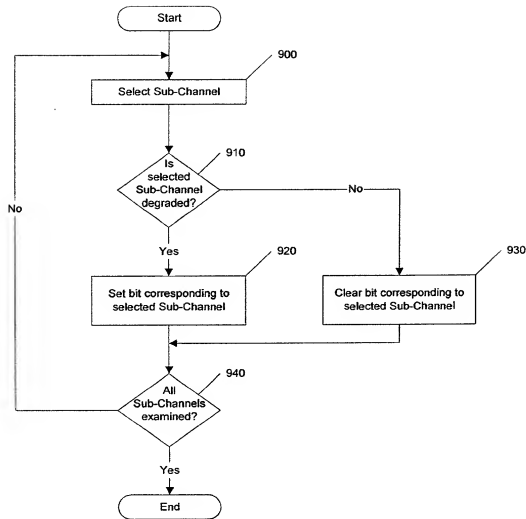


Fig. 9

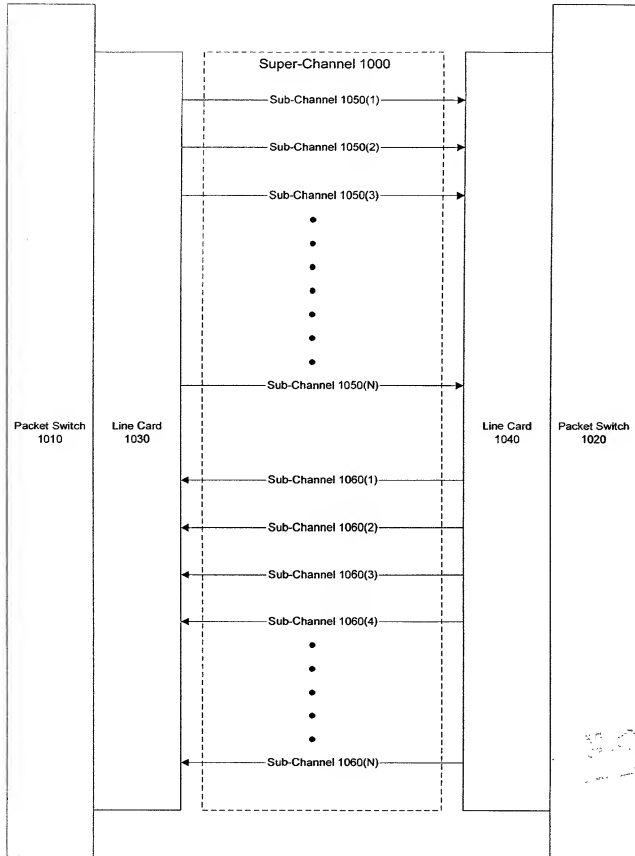


Fig. 10

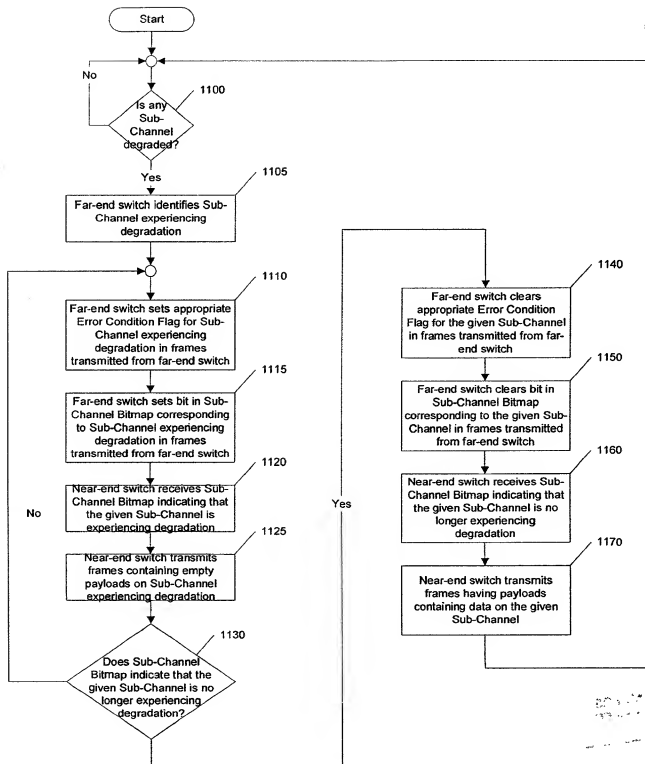


Fig. 11

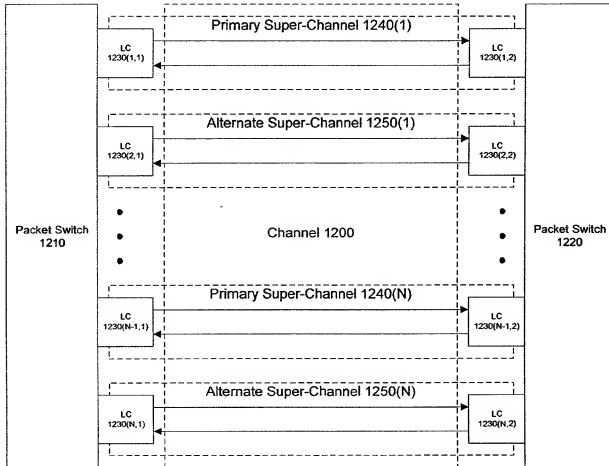


Fig. 12

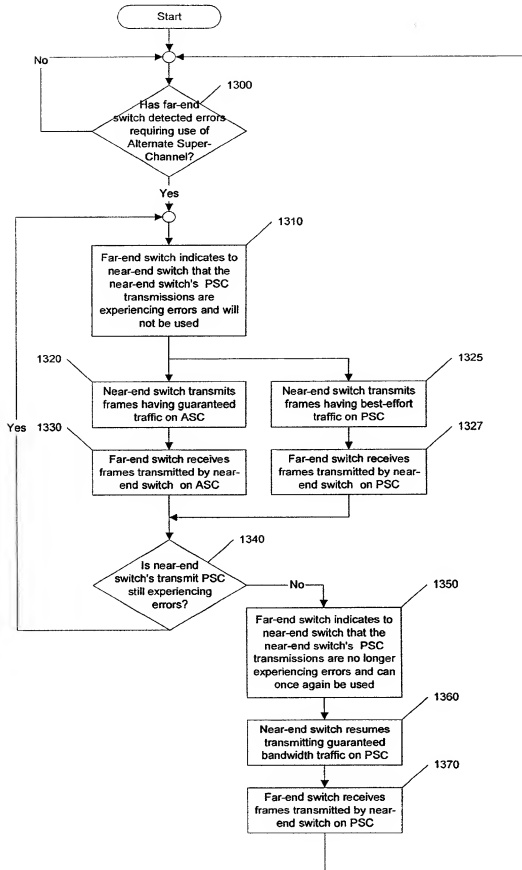


Fig. 13

10086273.071002

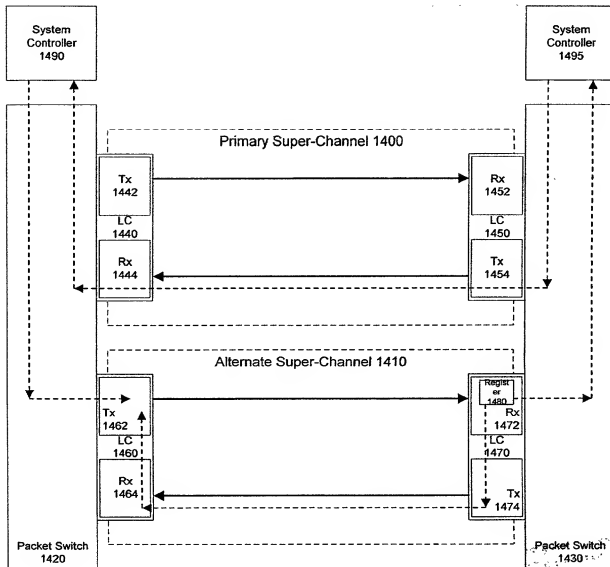


Fig. 14

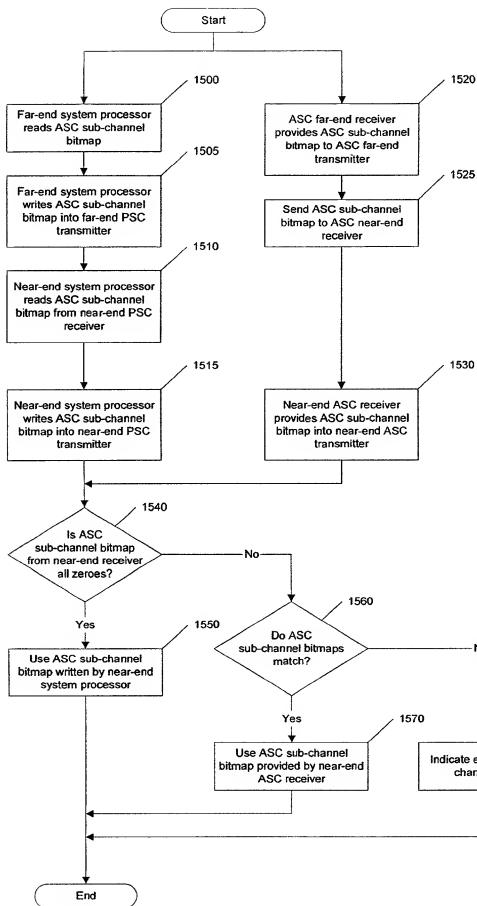


Fig. 15

10086273.07.1002

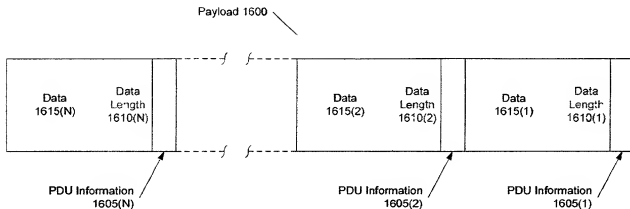


Fig. 16A

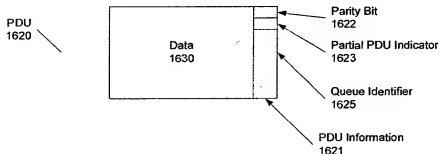


Fig. 16B

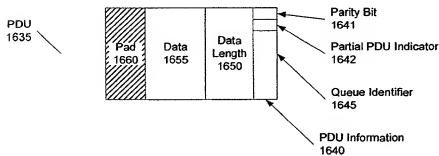


Fig. 16C

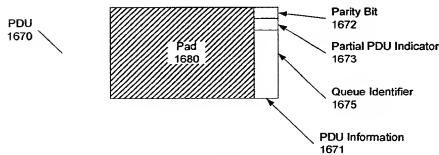


Fig. 16D

10086273.071002

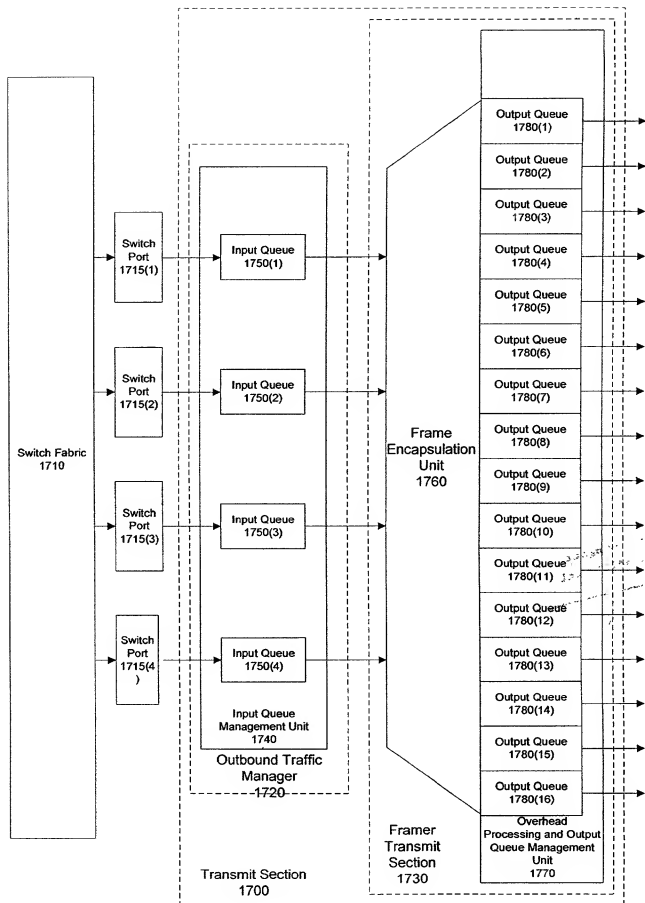


Fig. 17

Transmit Section
 1700

Input Queue 1750(1)											
A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1

Input Queue 1750(2)											
B12	B11	B10	B9	B8	B7	B6	B5	B4	B3	B2	B1

Input Queue 1750(3)								
C9	C8	C7	C6	C5	C4	C3	C2	C1

Input Queue 1750(4)							
D7	D6	D5	D4	D3	D2	D1	

Pad
 1810

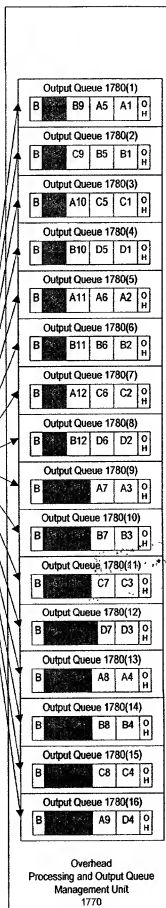
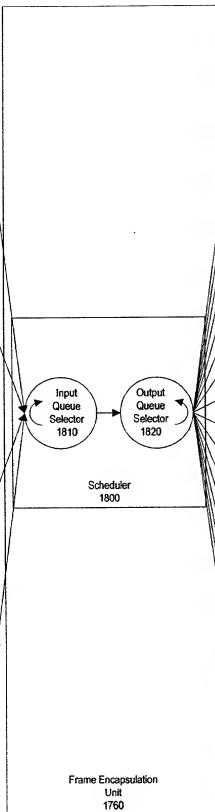


Fig. 18

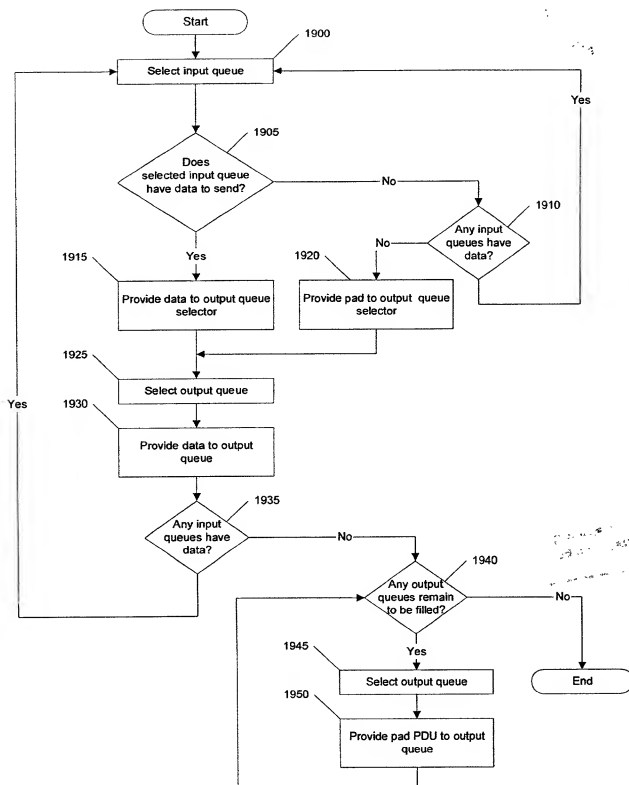


Fig. 19

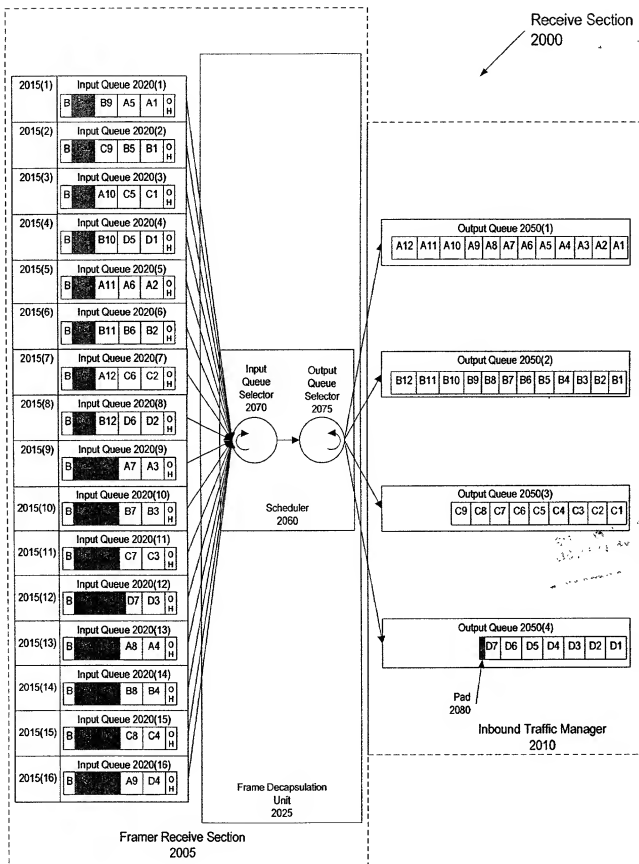


Fig. 20

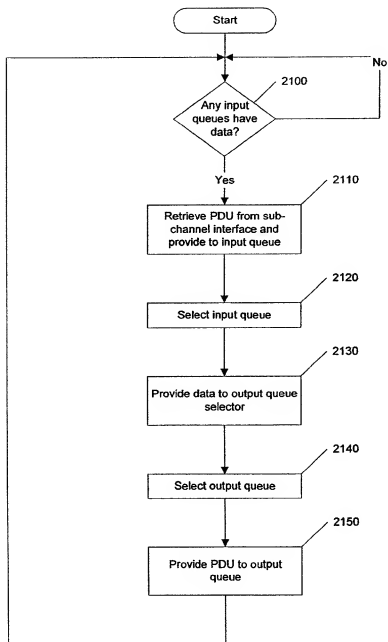


Fig. 21

10086273.071002

Sub-Channel Frame Synchronization
State Machine
2200

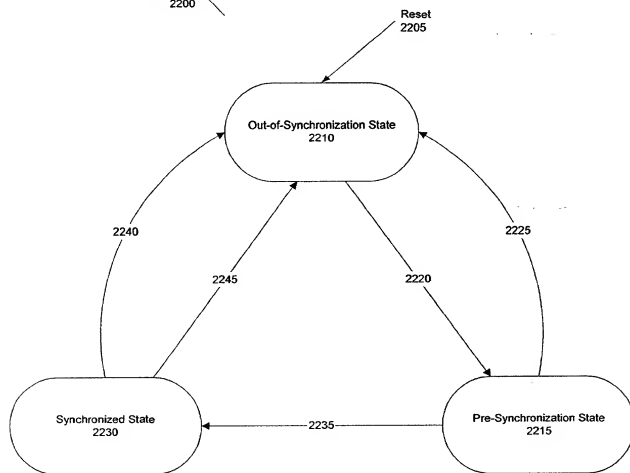


Fig. 22

10086273.071002

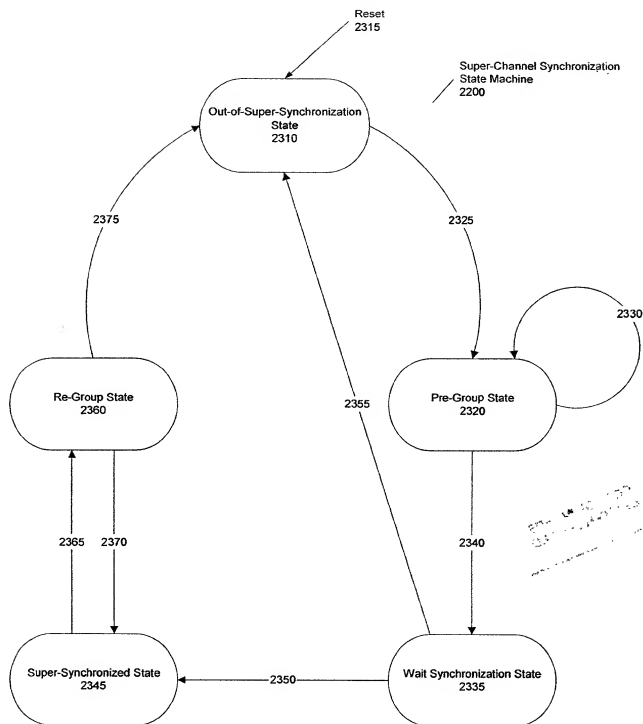


Fig. 23